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About the Author. KESHAB K. PARHI is Edgar F. Johnson Professor of Electrical and Computer Engineering at the University of Minnesota in Minneapolis. Dr. Parhi is widely recognized for his work in the area of VLSI digital signal and image processing.

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Pipelining is an important technique used in several applications such as digital signal processing (DSP) systems, microprocessors, etc. It originates from the idea of a water pipe with continuous water sent in without waiting for the water in the pipe to come out. Accordingly, it results in speed enhancement for the critical path in most DSP systems.

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