

# Vlsi Digital Signal Processing Systems Design And Implementation

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## VLSI Digital Signal Processing Systems

Book description. Digital audio, speech recognition, cable modems, radar, high-definition television-these are but a few of the modern computer and communications applications relying on digital signal processing (DSP) and the attendant application-specific integrated circuits (ASICs). As information-age industries constantly reinvent ASIC chips for lower power consumption and higher efficiency, there is a growing need for designers who are current and fluent in VLSI design methodologies for ...

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### VLSI Digital Signal Processing Systems: Design and ...

Pipelining is an important technique used in several applications such as digital signal processing (DSP) systems, microprocessors, etc.It originates from the idea of a water pipe with continuous water sent in without waiting for the water in the pipe to come out. Accordingly, it results in speed enhancement for the critical path in most DSP systems.

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1. ``VLSI Digital Signal Processing Syustems'', Keshab K. Parhi, Wiley Eastern 2. ``Digital Signal Processing for Multimedia Systems'', Keshab K. Parhi and Takao Nishitani, Marcel Dekker. 3. ``Pipelined Lattice and Wave Digital Recursive Filters'', J. G. Chung and Keshab K. Parhi, Kluwer.

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### **Pipelining (DSP implementation) - Wikipedia**

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Adaptive filters are often realized either as a set of program instructions running on an arithmetical processing device such as a Microprocessor or Digital Signal Processing (DSP) chip, or as a set of logic operations implemented in a field

programmable gate array (FPGA) or in Very Large Scale Integrated Circuit (VLSI).

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## **Keshab K. Parhi**

DSP systems can be realized using programmable processors or custom designed hardware circuits fabricated using very-large-scale-integrated (VLSI) circuit technology. The goal of digital design is to maximize the performance while keeping the cost down.

## **Scinapse | Academic search engine for paper**

VLSI digital signal processing systems: design and implementation. KK Parhi. John Wiley & Sons, 1999. 2364 \* 1999: VLSI architectures for discrete wavelet transforms. KK Parhi, T Nishitani. IEEE Transactions on Very Large Scale Integration (VLSI) Systems 1 (2), 191-202, 1993. 515: 1993:

## **VLSI Digital Signal Processing Systems: Design and ...**

He has published over 650 papers, is inventor or coinventor of 31 issued US Patents, has authored the text book VLSI Digital Signal Processing Systems: Design and Implementation (Wiley, 1999), and is the co-editor (with Takao Nishitani) of the reference book Digital Signal Processing for Multimedia Systems (CRC Press, March 1999).

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